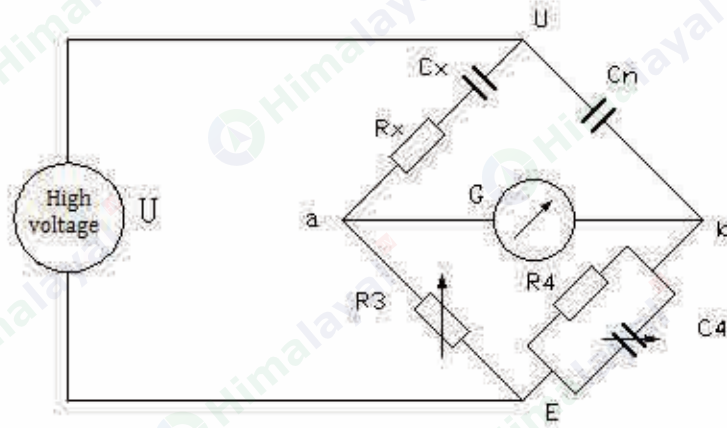


## Basic Operation Principles for High Voltage Capacitance Tan

### Delta Bridge

(1) Schering Bridge



Adjust R3, C4 to balance tan delta bridge, during which voltage at a and b pole is equal, which means same voltage at R3 and R4 end.

Capacitance impedance in AC circuit is  $\frac{1}{j\omega C}$ . Parallel impedance of R4, C4 in the circuit equals reciprocal of sum of R4 reciprocal and C4 reciprocal.

$$\frac{1}{\frac{1}{R4} + \frac{1}{\frac{1}{j\omega C4}}} = \frac{1}{\frac{1}{R4} + j\omega C4} = \frac{R4}{1 + j\omega R4C4}$$

On basis of principle of impedor partial pressure, it's easy to acquire:

$$U_a = \frac{R3}{\frac{1}{j\omega Cx} + R3} U = U_b = \frac{\frac{R4}{1 + j\omega R4C4}}{\frac{R4}{1 + j\omega R4C4} + \frac{1}{j\omega Cn}} U$$

Take reciprocal of both sides of the equation, we get:

$$\frac{1}{j\omega R3Cx} + \frac{R3}{R3} = \frac{1 + j\omega R4C4}{j\omega R4Cn} = \frac{1}{j\omega R4Cn} + \frac{C4}{Cn}$$

According to stipulation of equal complex number and real, imag equals respectively, we get:

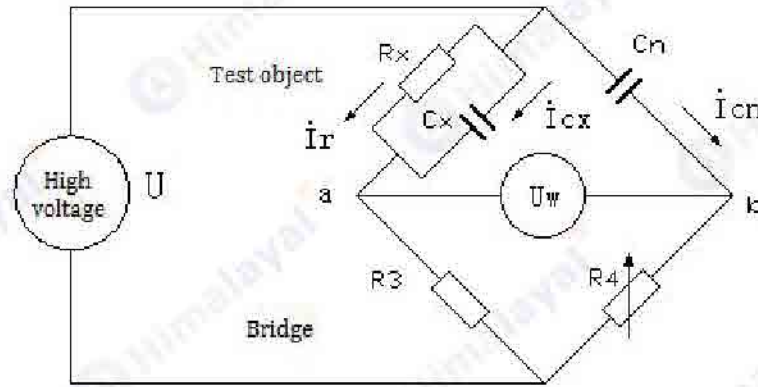
$$R_x = \frac{C4}{Cn} R3 \quad C_x = \frac{R4}{R3} Cn$$

Define  $\text{tg}\delta = \omega R_x C_x = \omega R_4 C_4$  based on parallel model dielectric loss, on account of fixed  $R_4$ ,

dielectric loss can be read from  $C_4$  dial,  $C_x$  can be calculated with  $R_3, R_4, C_n$ .

Instruments apply this principle include on-site QS1, laboratorial 2801, etc.

(2) M-type tan delta bridge



Change test object to parallel model. Please note that  $I_r$  forms a 90 angle with  $I_{cx}, I_{cn}$ :

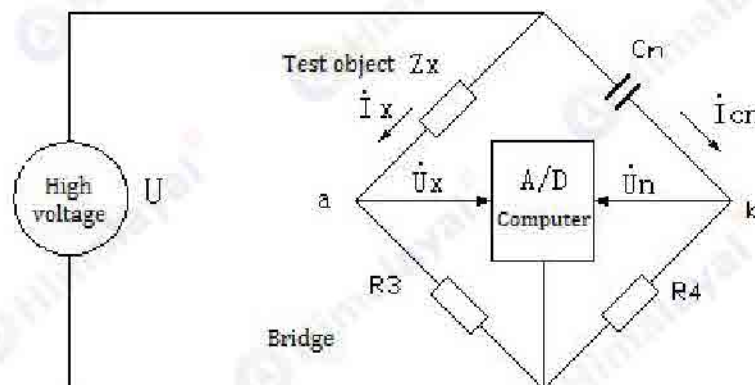
$$U_w = \sqrt{(I_{cn}R_4 - I_{cx}R_3)^2 + (I_rR_3)^2}$$

Adjust  $R_4$  to minimize  $U_w$ . During which  $I_{cn}R_4 = I_{cx}R_3, U_w = I_rR_3$ , thus:

$$\text{tg}\delta = \frac{I_r}{I_{cx}} = \frac{U_w}{I_{cn}R_4}$$

By reason that voltage between a and b isn't completely offset, therefore M-type tan delta bridge is also referred to as imbalance tan delta bridge.  $U_w$  measures absolute value, voltage is low under small dielectric loss, test precision can't be guaranteed.

(3) Digital tan delta bridge



Test circuit for digital tan delta bridge is still a bridge. Voltage at  $R_3$  and  $R_4$  end is sampled and

transferred to computer through A/D, acquire  $\dot{U}_x, \dot{U}_n$ .

$$\dot{I}_{cn} = \frac{\dot{U}_n}{R_4}, \quad \dot{I}_{cx} = \frac{\dot{U}_x}{R_3}, \quad \dot{U} = \dot{I}_{cn} \times \text{Standard capacitor impedance} = \frac{\dot{I}_{cn}}{j\omega C_n}$$

$$\text{Test object impedance } Z_x = \frac{\dot{U}}{\dot{I}_{cx}} = \frac{R_3}{R_4} \times \frac{\dot{U}_n}{\dot{U}_x} \times \frac{1}{j\omega C_n}$$

Acquire test object dielectric loss and capacitance through further calculation.

The biggest advantage of digital tan delta bridge lies in: automatic measurement, makes up for all theoretical error, no complicated mechanical adjustment component, software-oriented measurement, stable performance.